



Centre of Excellence in VLSI

VALUE PROPOSITION



- Maven Silicon is one of the top VLSI finishing schools now turned into a “Centre of Excellence in VLSI”
- Since 2010, we have deployed **3500+ VLSI Engineers** for the Semiconductor Industry: <https://www.maven-silicon.com/about-us>
- Recently awarded with a 2-year global contract for training **600 Staff / NCGs** on VLSI Design and Verification through our Online Cloud-based Learning Ecosystem by an Industry Leader in **Wi-Fi Tech Co. from USA**
- Concluded a Rapid VLSI Training Program for the **World’s Largest Mobility Solution Company in India**
- During October 2020, RISC-V designates Maven Silicon as a Global Training Partner: <https://www.maven-silicon.com/risc-v-trainings>

ABOUT US





Mr. Sivakumar is the Founder and CEO of **Maven Silicon**. He is responsible for the company's vision, overall strategy, business and technology.

He is also the Founder and CEO of **Aceic Design Technologies**.

Sivakumar is a seasoned engineering professional who has worked in various fields, including electrical engineering, academia and semiconductor industries for more than 2 decades. In semiconductor sector, he has worked as a Verification Consultant for the top EDA companies Synopsys, Cadence and Mentor and helped various ASIC and FPGA design houses deploy and use various verification methodologies effectively, resulting in successful tape out of SoCs and Chips.

He now specializes in offering Verification IPs and consulting services, EDA flow development and has delivered corporate trainings on advanced ASIC verification methodologies and technologies to IBM, NXP, Cypress, Broadcom, Qualcomm, ST Micro, AMD, AvagoTech, Wipro, Samsung, etc.

He is the recipient of the "**Outstanding Technical Achievement**" award from Cadence Design Systems and holds a degree in **Electrical and Electronics Engineering** from Madurai Kamaraj University.

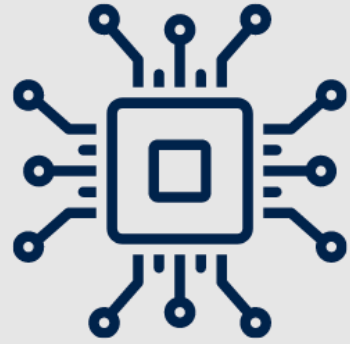
To know more about us: <https://www.maven-silicon.com/about-us>

THE LEADERSHIP

Mr. Sivakumar P R
Founder & CEO



WHY COE?



SKILL-UP TO
BEAT THE COMPETITION



GET YOUR STUDENTS
PLACED IN TOP MNCs



CERTIFICATION FOR
THE 'SMART' CV



SCHOLARSHIPS FOR
JOB-ORIENTED COURSES



FREE FDP PROGRAM



FREE ACCESS TO
WEBINAR ON
ADVANCED TOPICS

VLSI SoC Design using Verilog HDL Course

- This VLSI SoC Design Course using Verilog HDL is specially designed for Pre-final and final year Electronics / Electrical Engineering students **to learn and strengthen the knowledge on chip design process.**
- This course explains VLSI Technology, SoC Architecture and Design process, coding for synthesis and simulation.
- It explains the concept of hardware description language and basic concepts like data types and operators.
- Then it explains advanced concepts like assignments, procedural blocks, synthesis coding style and testbench coding.



INDUCTION SESSION



AUDIO/VIDEO LECTURES
BY INDUSTRY EXPERTS



CERTIFICATE ON
COMPLETION



HANDS-ON TRAINING
VIA LAB

HOW TO GET STARTED?



1. SIGN THE MOU



3. INDUCTION
SESSION
PLANNED



2. SHARE THE
STUDENT LIST FOR
THE COURSE
ACCESS



4. GET STARTED!

TESTIMONIALS

OUR TRAINEES



SAI TEJA K

Placed at Qualcomm

“

The **doubt clearing sessions** and the support were helpful during the course.

With the **Business Communication** classes, we were **prepared to face the professional world**. Thanks to the Placement team for their support even during the pandemic.

”



VARUN KUMAR

Placed at Samsung

“

I would appreciate a few special things like **extra lab working hours, and the technical team for clearing the doubts** every now and then. Placement team provides excellent job opportunities for the trainees which made me secure the placement in Samsung even before my course completion.

”



RAVITEJA REDDY

Placed at Intel

“

The videos of the course were very detailed, tools were provided to us to practice. The teaching staffs were always available to clarify my doubts. Upon completing the course, I got a job offer from INTEL. I thank Maven Silicon, for providing a great platform to learn design and verification aspects in VLSI.

”

TESTIMONIALS

GLOBAL CUSTOMERS



SanDisk®

“ The training was **well organized** and met the needs of the team with varied experience levels of the audience.
One of the key differentiators was that the sessions were very interesting and the labs provided were with apt examples which the engineers can apply and use while implementing in live projects.”

”



QUALCOMM®

“ **Excellent and customized training** as per the need of our diversified audience [2-8 years Exp]
The **trainer had in-depth knowledge** of course contents, made sure that all the important concepts of SV & UVM are conveyed in the stipulated time. The labs provided were with **appropriate examples** which the engineers can apply and use while implementing in live projects.”

”



CYPRESS

“ **Excellent training** on “SystemVerilog for Verification” which is tailored to the needs of our organization.
Training courses are specific and **relevant to the audience** of the training – be it NCGs or experts. The training was **interactive** and we found it **extremely beneficial** to the team.”

”



CONNECT WITH US

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ASSOCIATION & PARTNERSHIPS



Together

We Grow